

HP-41C Matrix Multiplication by John H. Nickel
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Includes corrections from NOP: V9N8P3, V10N4P2

```
01 LBL "M*M" ;Matrix Multiplication Program
02 LBL 00
03 RCL 01
04 STO 03
05 LBL 01
06 RCL 02
07 STO 04
08 0
09 STO 05
10 LBL 02
11 9
12 XEQ 03
13 RCL 03
14 RCL 04
15 FS? 00
16 X<>Y
17 XROM 20,36 ;M5 - PPC ROM Matrix, (i,j) to Register Address
18 RCL IND X
19 STO 06
20 10 ;Correction per NOP V10N4P2 by T.M. Morgan
21 XEQ 03
22 RCL 04
23 RCL 00
24 FS? 01
25 X<>Y
26 XROM 20,36 ;M5 - PPC ROM Matrix, (i,j) to Register Address
27 RCL IND X
28 RCL 06
29 *
30 ST+ 05
31 ISG 04
32 GTO 02
33 11
34 XEQ 03
35 RCL 03
36 RCL 00
37 XROM 20,36 ;M5 - PPC ROM Matrix, (i,j) to Register Address
38 RCL 05
39 STO IND Y
40 ISG 03 ;Omitted in PPC V9N5P17, corrected NOP V9N8P3
41 GTO 01
42 ISG 00
43 GTO 00
44 CF 00
45 CF 01
46 RTN
47 LBL 03
48 RCL IND X
49 STO 07
50 FRC
51 E3
52 *
53 STO 08
54 END
```

0059C000F4004D2A4D01213302223410350319E000032324AC
0071A52490F3361110E000032420AC0171A52490F326429205
9604B3001111E000032320A5242591F29603B2009600B100A9
00A901850490F337691B134238C0000D65

89 BYTES

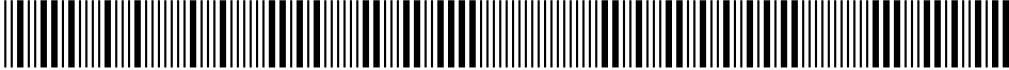
HP-41C Matrix Multiplication by John H. Nickel PPC V9 N5 P17 Aug 1982

Program Registers Needed: 13

Row 1 (1 - 7)



Row 2 (8 - 17)



Row 3 (17 - 24)



Row 4 (25 - 32)



Row 5 (33 - 40)



Row 6 (40 - 47)



Row 7 (48 - 54)

